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(71) Applicant(s):

SONY COMPUTER ENTERTAINMENT INC. [JP/JP]; 2-6-21, Minami-Aoyama, Minato-ku, Tokyo 1070062 (JP) *(for all designated states except US)*

SUZUOKI, Masakazu [JP/JP]; c/o SONY COMPUTER ENTERTAINMENT INC., 2-6-21, Minami-Aoyama, Minato-ku, Tokyo 1070062 (JP) *(for US only)*

(72) Inventor(s):

SUZUOKI, Masakazu; c/o SONY COMPUTER ENTERTAINMENT INC., 2-6-21, Minami-Aoyama, Minato-ku, Tokyo 1070062 (JP)

(74) Agent(s):

SUZUKI, Seigoh; Pacific City Shiba Bldg. 6th Floor 29-10, Shiba 2-chome Minato-ku, Tokyo 105-0014 (JP)

(54) Title (EN): METHODS AND APPARATUS FOR INSTRUCTION SET EMULATION

(54) Title (FR): PROCEDES ET APPAREIL D'EMULATION D'ENSEMBLE D'INSTRUCTIONS

(57) Abstract:

(EN): Methods and apparatus provide for translating a software program page by page from a first instruction set architecture (ISA) into a second ISA using one or more of a set of processors of a multi-processor system; and executing the translated software program using a dedicated other processor of the multi-processor system.

(FR): L'invention concerne des procédés et un appareil permettant de traduire un programme logiciel page par page, d'une première architecture d'ensemble d'instructions (ISA) vers une seconde ISA, à l'aide d'au moins un ensemble de processeurs d'un système multiprocesseur ; et d'exécuter le programme logiciel traduit à l'aide d'un autre processeur spécialisé du système multiprocesseur.

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