

**Information on Search Strategy - Pilot phase (see OJ 2015, A86)**

The type of information contained in this sheet may change during the pilot for improving the usefulness of this new service.

Application Number

PCT/EP2020/087570

TITLE: CONTROL OF DATA TRANSFER BETWEEN PROCESSORS

APPLICANT: GRAPHCORE LIMITED

IPC CLASSIFICATION: G06F15/173, G06N3/063

EXAMINER: De Poy, Iker

CONSULTED DATABASES: INET, WPI, ANSERA

CLASSIFICATION SYMBOLS DEFINING EXTENT OF THE SEARCH:

IPC:

CPC: G06F15/17325, G06N3/063

FI/F-TERMS:

KEYWORDS OR OTHER ELEMENTS FEATURING THE INVENTION:

P: Prevent processors that exchange data from running ahead of one another in the overall exchange process between processors of a system

S: Data processing system comprising a plurality of processors, wherein each of the processors perform data transfer operations during exchange stages. By counting an amount of data received from another processor before performing data transfer operations, a processor is prevented from running ahead of the other processor and transferring data to the other processor before the other processor is ready to receive the further outgoing data.