



(51) International Patent Classification:  
*H04N 21/80* (2011.01)

(21) International Application Number:  
PCT/US2018/043555

(22) International Filing Date:  
24 July 2018 (24.07.2018)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
15/693,184 31 August 2017 (31.08.2017) US

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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JO, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

(54) Title: LOW LATENCY AUDIO STREAM ACCELERATION BY SELECTIVELY DROPPING AND BLENDING AUDIO BLOCKS

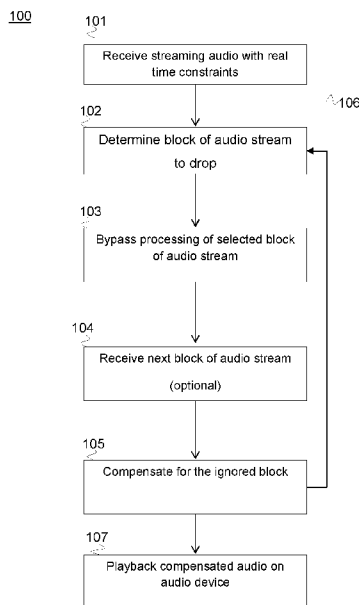


FIG. 1

(57) Abstract: A method and device for accelerated audio processing in a streaming environment. The method comprises receiving a streaming audio asset, locating a position to ignore processing of an audio block of the streaming audio asset, ignoring the audio block, compensating for the ignored audio block and playing the compensated audio on an audio device.



**Published:**

— *with international search report (Art. 21(3))*

# **LOW LATENCY AUDIO STREAM ACCELERATION BY SELECTIVELY DROPPING AND BLENDING AUDIO BLOCKS**

## **FIELD OF THE DISCLOSURE**

The current disclosure relates to streaming audio programs. More specifically the current disclosure relates to accelerated processing of time constrained audio streams.

## **BACKGROUND OF THE INVENTION**

In a streaming audio asset there are often times constraints on processing of the audio asset such that a large buffer of audio information cannot be maintained. These constraints may either be due to physical limitations of the system or constraints within the program streaming the audio asset.

One such situation where there are constraints on the amount of information that can be buffered is in the context of cloud based emulation. In a cloud-based emulation system the majority of the processing takes place on the remote cloud-based server. This allows the client device platform that is communicating with the cloud-based server to use less resources for processing the cloud based game. Current techniques implement incremental buffering to make the game load faster and parallel processing between the server and the client. However as a result of this arrangement communication between the client device and the server must be synchronized and the client device can only maintain a small buffer to of emulated information. If the client device and the server are not synchronized it may result in unusual or unexpected device behavior. Sometimes audio blocks that are sent through the network are lost or the system requires additional time to process the audio which results in a loss of synchronization. Thus there is a need in the art for a way to accelerate processing audio to ensure synchronization and improve loading times for audio assets.

## **BRIEF DESCRIPTION OF THE DRAWINGS**

The teachings of the present invention can be readily understood by considering the following detailed description in conjunction with the accompanying drawings, in which:

FIG. 1 is a flow diagram of a method for accelerated processing of audio according to aspects of the present disclosure.

FIG. 2 is a line graph of a prior art sound wave according to an aspect of the present disclosure.

FIG. 3 is a line graph of a sound wave with a dropped audio block according to an aspect of the present disclosure.

FIG. 4 is a line graph of a sound wave with a dropped audio block and compensation with line fitting according to an aspect of the present disclosure.

FIG. 5 is a line graph of a soundwave with two dropped audio blocks and compensation according to aspects of the present disclosure.

FIG 6 is a block diagram of a system that implements the method for audio acceleration according to aspects of the present disclosure.

FIG 7 is a flow diagram showing the method for determining the length of audio levels to modify using wave for analysis according to aspects of the present disclosure.

FIG 8 is a flow diagram showing the method for determining the location to drop an audio block using signal analysis according to aspects of the present disclosure.

FIG 9 is a flow diagram depicting a method for determining blocks to be dropped according to aspects of the present disclosure.

FIG 10 is a line graph of a sound wave with dropped blocks within a sliding priority frame according to aspects of the present disclosure.

## INTRODUCTION

Depending on the frequency of the sound the human ear can distinguish meaningful short changes in frequency as fast as 2ms. Though research in this field is ongoing some limits have been found empirically. Detection of meaningful changes in frequency by the human ear varies greatly depending on the frequency of the waveform in question. For example an audible low frequency bass signal at 40 (Hertz) Hz measures 25 milliseconds (ms) crest to crest. It would take more than 50 ms to produce a meaningful sound that would be recognized as something other than noise. Conversely a high frequency waveform of 15 kilohertz (kHz) becomes recognizable sound much sooner, at around 2 ms. When streaming audio a continuous

soundwave is parsed in to samples which are packaged in to discrete packets or audio blocks and sent over the network to a client. The client device receives the audio block and processes it to place the audio block in a form which is readily playable on the client device. In prior art systems the client device may include a large buffer of audio blocks which are processed before playback. In this way prior art systems were able to maintain synchronization of playback and processing because playback is generally slower than processing and any time processing slowed down there was a large buffer before playback needs to stop and wait for processing to finish. There are situations where systems cannot maintain a large buffer for example in systems with small amounts of physical memory or in systems that must maintain synchronization with a server; these limitations are referred to herein as real time constraints. In these instances audio processing may be accelerated if the system can select and drop certain audio blocks before they are processed without a user noticing the effects of the dropped audio block.

#### DETAILED DESCRIPTION

Although the following detailed description contains many specific details for the purposes of illustration, anyone of ordinary skill in the art will appreciate that many variations and alterations to the following details are within the scope of the invention. Accordingly, various implementations of aspects of the present disclosure described below are set forth without any loss of generality to, and without imposing limitations upon, the claimed invention.

FIG 1 shows a method **100** for selectively dropping and blending audio blocks for acceleration of an audio stream. Initially a device may receive or generate an audio stream, as indicated at **101**. The audio stream may be composed of a series of packets or audio blocks containing audio information. The packets or audio block size may be predefined by the networking protocol or underlying standard for convenience and to provide minimal latency impact. As an added benefit these packet or block sizes are usually well aligned with the minimum audio latency response of the underlying systems. In other embodiments the packet or audio block size may be defined by the system to maximize the perceived audio quality when dropping blocks according to aspects of the present disclosure. It should be noted that using audio block sizes that are not a factorial of the block size of the system inherently increases the latency of processing. Additionally the audio stream may be subject to real time constraints that limit the amount of buffered

information that may be kept for the stream or the amount of time before the audio is played back.

The device may determine a location in the audio stream to drop an audio block, as indicated at **102** such as an audio block that has no dependencies or a block for which there is or is not a large audio level difference between the end of previous block and the beginning of the next block. In one embodiment the location to drop an audio block is determined from signal analysis which may better be understood with respect to FIG 7. The location to drop a block may be determined by transforming the waveform **701** into frequency and time domains using known methods such as Fourier transform (DFT), discrete cosine transform (DCT), or discrete wavelet transform (DWT) analysis. In analysis of the transform, the wave spectra of each audio block is chopped **702** in to wavelets and distinct the change between wavelets is measured **703**, blocks that exhibit a high degree of changes over time are flagged and may not be dropped **704**. A high degree of rate of change may be determined by a threshold. The device may be configured to drop audio blocks at a set interval to accelerate audio processing hereinafter referred to as a drop interval. In an embodiment if the block determined to be dropped by the block interval is flagged as having a high rate of change the system will choose the next block in the stream, in a different embodiment the system may choose the previous block in the stream. The drop interval may be determined by the system based on latency constraints for example when the system determines high latency in processing blocks, the drop interval may be reduced. Conversely the system may increase the drop interval when it is determined that the latency is low. In an alternative embodiment the device may have a predetermined threshold for audio levels and when the audio level is below that threshold the device may drop blocks. The device may also have some combination of audio level threshold and drop interval.

FIG. 9 shows a method for determining blocks to be dropped according to an embodiment of the present disclosure. The method may begin with determining the drop interval **902** from latency information as discussed above. Signal analysis as discussed above may be used to transform the waveform into frequency time domain waves **901**, break the waves into wavelets **903** and analyze the rate of change of the wavelets **904**. From the rate of change information a drop priority flag may be applied to each audio block **905** which indicates the amount of changes over time. Blocks with a high degree of change will be flagged as low priority blocks whereas blocks

with a low degree of change will be flagged with a higher priority. The system may use the drop interval to set a sliding priority window **906**. The system applies the drop priority flags to each of the blocks within the sliding priority window **907**. Within the sliding priority window the system determines which blocks drop **908** by comparing the drop priority flag values of each block. The block with the highest priority **909**(i.e. the block with the lowest amount of changes over time) will be dropped and the other lower priority blocks will be kept **910**. The size of the priority window can be of any length but it is preferable that the perceived latency of the system is reduced. Ideally the priority window is under 40ms. In this case, the drop interval may set the length of time between each priority window.

Aspects of the method of FIG 9 may better be understood with respect to FIG 10. A waveform **1001** of streaming audio content is depicted. Line **1003** represents the amount of streaming audio that has been stored in memory. The **1004** and **1005** represent the sliding priority frame. The sliding priority frames occur every drop interval **1008**. Within the sliding priority frame the block with the highest drop priority is dropped **1009**.

Once the device has determined that dropping an audio block is appropriate, the device may start dropping audio blocks. To drop an audio block the device may bypass processing of the block, as indicated at **103**. Alternatively the device may initially receive the unprocessed audio block in a buffer and allow the unprocessed audio block to be over-written by the next audio block. The device may then receive the next audio block after the dropped block, as indicated at **104** and process it for playback. A key concept of the present disclosure is that the described system does not add any additional latency to the processing of audio received over the network. Once an audio packet has been received from the network and decompressed, the client can analyze and cut from the stream without incurring any additional latency.

The device may compensate for the dropped audio block **105** by reducing the difference in audio level between the end of the prior audio block and the beginning of the next audio block. By way of example and not by way of limitation the device may simply average the last set samples of the prior audio block with initial set sample of the next audio block and adjust the values of those samples towards that average. In some embodiments only the end of the prior audio block is averaged. In other embodiments only the beginning of the next audio block is averaged. The

number of samples used for the average may be determined empirically to find the most natural sounding way to bridge a dropped block. For example it has been found that pops and clicks are effectively removed by averaging audio of only 0.02ms in length. In other embodiments wave form analysis is performed to determine the length of audio levels to modify as seen in FIG 6. The frequency of the previous audio block or blocks is analyzed **601**. If the analysis determines **602** that the wave form is low frequency **603**, a longer period of averaging may be chosen **605** for a better match to the original spectra. Conversely if the waveform is determined **602** to be high frequency **604** then a very short period of averaging or no averaging will be chosen **606**. By way of example and not by way of limitation for a low frequency waveform the system may choose an averaging period equal to or less than 0.06ms and for high frequency waveforms the system may choose an averaging period equal to or less than 0.005ms. According to alternate aspects of the present disclosure there may be greater than two averaging periods, for example there may be averaging periods for low, middle and high frequency waveforms or there may be a number of averaging periods corresponding to wave form frequencies chosen to best approximate the underlying waveforms without noticeably interfering with the user experience.

In some implementation the gap in audio level created by the dropped block may be filled by interpolating the waveform using wavelet analysis. In some embodiments the length of interpolation is chosen based on the frequency of the wave form. As discussed above a longer period of the interpolation calculation will be chosen for a lower frequency waveform and short period of interpolation will be chosen for a high frequency audio wave form.

Once the device has dropped a block and compensated for the dropped block as discussed above it may play the audio, as indicated at **107** and/or return to receiving and/or generating audio blocks as normal, as indicated at **106**.

FIG. 2 shows graph of audio level versus time depicting 100 milliseconds of a sound wave **201**. In this depiction the wave is shown as a continuous line but it should be understood that in certain embodiments the wave may be composed of a multiple samples of audio information. For example a typical Compact Disk quality MP3 audio file is composed of 44100samples per second.



FIG. 3 depicts a soundwave **301** with a dropped block **302** according to aspects of the present disclosure. In the illustrated example, the dropped block **302** lasts for 5 milliseconds but in other embodiments the block may be as short as 2 milliseconds or as long as 25 milliseconds. In this depiction the audio is streaming as represented by the dotted line **303**. The device already decided to drop a block **302** and has received the next audio block **304** after the dropped block **302**. The device has not yet compensated for the dropped block **302**.

It should be understood that there is no minimum length for dropped blocks but as the number of dropped blocks increase and the length of the dropped blocks shortens the human ear begins to perceive the sound at a higher frequency. For example if the dropped block length was 0.01 ms and every other block was dropped the perceived audio would sound accelerated and at a very high pitch. Currently the optimal block length is between 3ms and 7ms. In current multimedia systems block lengths of greater than 16ms are not found because 16ms corresponds to the current industry standard for video of 60 frames per seconds.

FIG. 4 depicts a soundwave **401** with a dropped block **402** and compensation according to aspects of the present disclosure. In the disclosed embodiment the tail end of the previous block **404** and the beginning of the next block **405** have been averaged to eliminate user perception of the dropped block. By averaging the beginning and end of the blocks the user's perception of clicks and pops may be reduced or eliminated completely. The dashed line represents the original audio **406** and is included to show the difference between the original audio **406** and the dropped block-compensated audio **401**.

FIG 5 shows a soundwave **501** with two dropped blocks **502**, **509** and compensation according to aspects of the present disclosure. In this embodiment the device has streamed 94 ms of audio **503**. The device is configured with a drop interval **508** by way of example and not by way of limitation the device may be configured to drop blocks at a 50 ms interval. The interval may be determined experimentally to find the optimal number of dropped blocks to provide desired level of audio acceleration without a perceived audio quality loss. The device has identified and dropped a first audio block **502** and averaged the end of the previous audio block **504** with the beginning of the next audio block **505** and averaged the beginning of the next audio block **505** with the previous audio block **504** to compensate for the dropped audio. The device has then

continued streaming audio till the end of the interval **508**. At which point the device drops another audio block **509**. After receiving the beginning of the next audio block **507** the device averages the end of the previous un-dropped audio block **506** by the beginning of the next audio block **507** and the beginning of the next audio block **507** with the end of the previous audio block **506** to compensate for the dropped block. At which point the device is able to play back the compensated audio.

The block diagram shown in FIG. 6 schematically illustrates certain aspects of the present disclosure within the particular context of remote emulation. In this example, an emulator **607** may be accessed by a device **602** over a network **660**. The device **602** may access alternative remote emulators **607** over the network **660** (herein referred to as remote emulators). Emulators **607** may be identical to each other, or they may each be programmed to emulate unique legacy programs **606** including unique legacy games **606**.

The device **602** may include a central processor unit (CPU) **631**. By way of example, a CPU **631** may include one or more processors, which may be configured according to, e.g., a dual-core, quad-core, multi-core, or Cell processor architecture. The device **602** may also include a memory **632** (e.g., RAM, DRAM, ROM, and the like). The CPU **631** may execute a process-control program **633**, portions of which may be stored in the memory **632**. The device **602** may also include well-known support circuits **640**, such as input/output (I/O) circuits **641**, power supplies (P/S) **642**, a clock (CLK) **643** and cache **644**. The device **602** may optionally include a mass storage device **634** such as a disk drive, CD-ROM drive, tape drive, or the like to store programs and/or data. The device **602** may also optionally include a display unit **637** and a user interface unit **638** to facilitate interaction between the device **602** and a user who requires direct access to the device **602**. The display unit **637** may be in the form of a cathode ray tube (CRT) or flat panel screen that displays text, numerals, or graphical symbols. The user interface unit **638** may include a keyboard, mouse, joystick, light pen, or other input devices. Additionally the device may include an audio device **670** for the playback or recording of audio waveforms. Such audio devices may include speakers, microphones, oscilloscopes, phonographs and other sound playback/recording media. The device **602** may control the audio device through appropriate driver software, which may be stored in the memory **632** and/or storage device **634** and executed

by the CPU **631**. The device **602** may include a network interface **639**, configured to enable the use of Wi-Fi, an Ethernet port, or other communication methods.

The network interface **639** may incorporate suitable hardware, software, firmware or some combination of two or more of these to facilitate communication via an electronic communications network **660**. The network interface **639** may be configured to implement wired or wireless communication over local area networks and wide area networks such as the Internet. The device **602** may send and receive data and/or requests for files via one or more data packets over the network **660**.

The preceding components may exchange signals with each other via an internal system bus **650**. The device **602** may be a general purpose computer that becomes a special purpose computer when running code that implements embodiments of the present invention as described herein.

The emulator **607** may include a central processor unit (CPU) **631'**. By way of example, a CPU **631'** may include one or more processors, which may be configured according to, e.g., a dual-core, quad-core, multi-core, or Cell processor architecture. The emulator **607** may also include a memory **632'** (e.g., RAM, DRAM, ROM, and the like). The CPU **631'** may execute a process-control program **633'**, portions of which may be stored in the memory **632'**. The emulator **607** may also include well-known support circuits **640'**, such as input/output (I/O) circuits **641'**, power supplies (P/S) **642'**, a clock (CLK) **643'** and cache **644'**. The emulator **607** may optionally include a mass storage device **634'** such as a disk drive, CD-ROM drive, tape drive, or the like to store programs and/or data. The emulator **607** may also optionally include a display device **637'** and user interface unit **638'** to facilitate interaction between the emulator **607** and a user who requires direct access to the emulator **607**. By way of example and not by way of limitation a device or engineer **602** may need direct access to the emulator **607** in order to program the emulator **607** to properly emulate a desired legacy program **606** or to add additional capabilities to a legacy program **606**. The display device **637'** may be in the form of a cathode ray tube (CRT) or flat panel screen that displays text, numerals, or graphical symbols. The user interface unit **638'** may include a keyboard, mouse, joystick, light pen, or other device. The emulator **607** may include a network interface **639'**, configured to enable the use of Wi-Fi, an Ethernet port, or other communication methods.

The network interface **639'** may incorporate suitable hardware, software, firmware or some combination of two or more of these to facilitate communication via the electronic communications network **660**. The network interface **639'** may be configured to implement wired or wireless communication over local area networks and wide area networks such as the Internet. The emulator **607** may send and receive data and/or requests for files via one or more data packets over the network **660**.

The emulator **607** may be a general purpose computer that becomes a special purpose computer when running code that implements embodiments of the present invention as described herein.

Emulator **607** may access a legacy program **606** that has been selected by the device **602** for emulation through the internal system bus **650'**. There may be more than one legacy program **606** stored in the emulator. The legacy programs may also be stored in the memory **632'** or in the mass storage device **634'**. Additionally, one or more legacy programs **606** may be stored at a remote location accessible to the emulator **607** over the network **660**. Each legacy game **606** contains game code and legacy audio**608**.

The game code **608** may contain un-accelerated audio that is sent from the emulator **607** to the device **602**. The device **602** may have real time constraints and implement the disclosed method for accelerating audio **100**.

While the above is a complete description of the preferred embodiment of the present invention, it is possible to use various alternatives, modifications and equivalents. Therefore, the scope of the present invention should be determined not with reference to the above description but should, instead, be determined with reference to the appended claims, along with their full scope of equivalents. Any feature described herein, whether preferred or not, may be combined with any other feature described herein, whether preferred or not. In the claims that follow, the indefinite article "A", or "An" refers to a quantity of one or more of the item following the article, except where expressly stated otherwise. The appended claims are not to be interpreted as including means-plus-function limitations, unless such a limitation is explicitly recited in a given claim using the phrase "means for."

## WHAT IS CLAIMED IS:

- 1 1. A method for accelerated audio processing in a streaming environment the method  
2 comprising:
  - 3 a) locating a position to ignore processing of an audio block of a streaming audio asset;
  - 4 b) ignoring the audio block chosen in a);
  - 5 c) compensating for the ignored audio block to generate compensated audio;
  - 6 d) playing the compensated audio through an audio device.
- 1 2. The method of claim 1 wherein a) includes I) deriving a transformation of the audio block, II)  
2 breaking the audio block transform into wavelets and III) deriving the rate of change of the  
3 wavelets..
- 1 3. The method of claim 2 further comprising IV) flagging at least one audio block with a drop  
2 priority based on the rate of change of the wavelets.
- 1 4. The method of claim 3 wherein an audio block prior or after, the audio block flagged with a  
2 drop priority, is ignored.
- 1 5. The method of claim 3 further comprising establishing a sliding priority window and  
2 comparing the audio blocks within the sliding priority window to determine which block is  
3 flagged with highest drop priority, wherein the audio block with the highest drop priority is  
4 ignored in b) and wherein the drop priority describes the amount of change in the audio block
- 1 6. The method of claim 5 further comprising receiving additional audio blocks in the streaming  
2 audio asset and repeating steps a) through d) at a predetermined interval
- 1 7. The method of claim 1 further comprising receiving additional audio blocks in the streaming  
2 audio asset and repeating steps a) through d) at a predetermined interval.
- 1 8. The method from claim 1 wherein locating a position at a) comprises determining whether the  
2 audio satisfies a threshold condition.
- 1 9. The method from claim 1 wherein locating a position at a) comprises determining whether the  
2 audio block is dependent on another audio block or data source.
- 1 10. The method of claim 1 wherein the ignored audio block in b) is less than 5ms long.

1 11. The method of claim 1 wherein compensating at d) comprises averaging an end of a prior  
2 audio block that is immediately before the ignored audio block with a beginning of the next  
3 audio block.

1 12. The method of claim 11 wherein the beginning of the next audio block is averaged with the  
2 end of the prior audio block.

1 13. The method of claim 11 wherein an amount of the end of the prior audio block and the  
2 beginning of the next audio block that is averaged is between 0.005milliseconds (ms) and 0.06ms  
3 respectively.

1 14. The method of claim 11 wherein frequency analysis is applied to the audio block  
2 immediately before the ignored audio block to determine the amount of the end of the prior audio  
3 block to be averaged with the beginning of the next audio block.

1 15 The method of claim 14 wherein the frequency next audio block is also analyzed to determine  
2 the amount of the beginning next audio block to be averaged with the end of the prior audio  
3 block.

1 16. The Method of Claim 15 wherein a greater amount of the end of the prior audio block and the  
2 beginning of the next audio block is averaged at a lower frequency than at a higher frequency.

1 17. The Method of Claim 15 wherein a smaller amount of the end of the prior audio block and  
2 the beginning of the next audio block is averaged at a higher frequency than at a lower  
3 frequency.

1 18. The Method of Claim 15 wherein at a higher frequency the end of the prior audio block and  
2 the beginning of the next audio block are not averaged.

1 19. The method of claim 1 wherein the audio device is a videogame console.

1 20. A device, comprising:

2 a CPU;

3 an audio device;

4 a Memory coupled to the CPU; and

5 non-transitory instructions embedded in memory which when executed cause the CPU to enact  
6 the method for accelerated processing of audio comprising:

7 a) locating a position to ignore processing of an audio block of the streaming audio asset;

- 8           b) ignoring the audio block chosen in a);  
9           c) compensating for the ignored audio block;  
10          d) playing the compensated audio through the audio device.

1   21. A non-transitory computer-readable medium having embodied therein coded instructions  
2   executable by a computer processor, the coded instructions being configured to implement a  
3   method for accelerated processing of audio, the method comprising:

- 4           a) locating a position to ignore processing of an audio block of the streaming audio asset;  
5           b) ignoring the audio block chosen in b);  
6           c) compensating for the ignored audio block;  
7           d) playing the compensated audio on an audio device.

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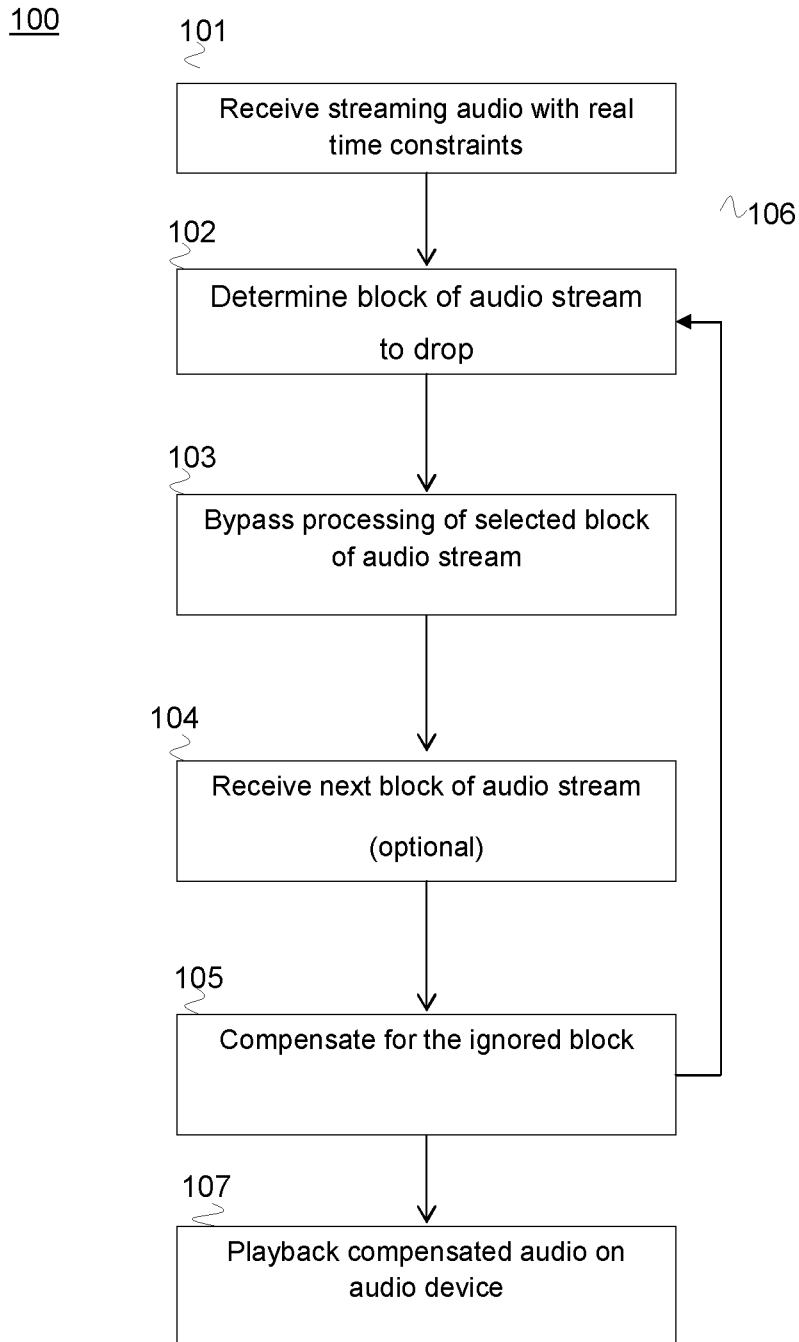


FIG. 1



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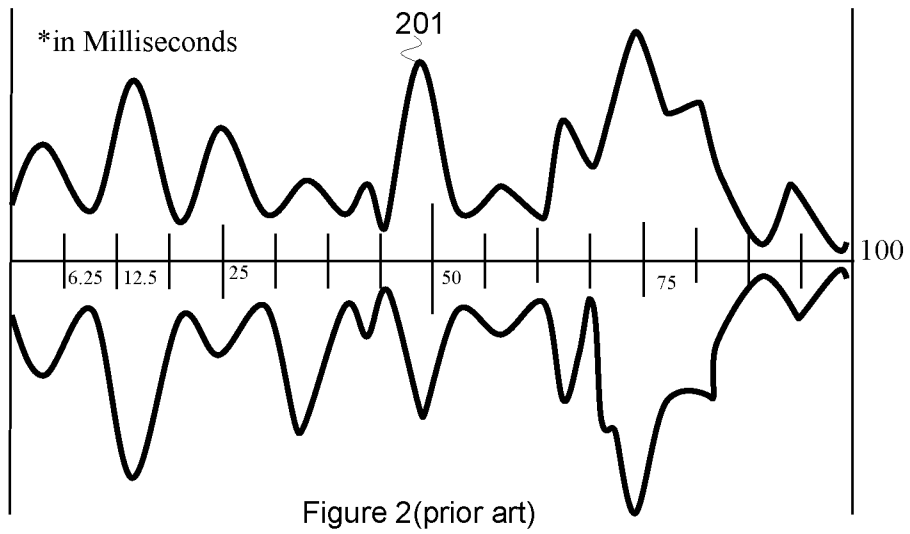


Figure 2(prior art)

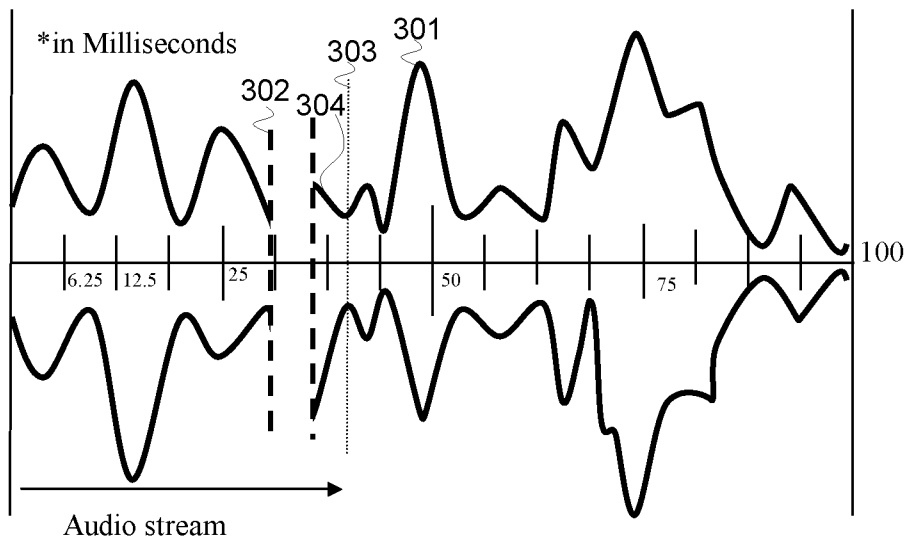
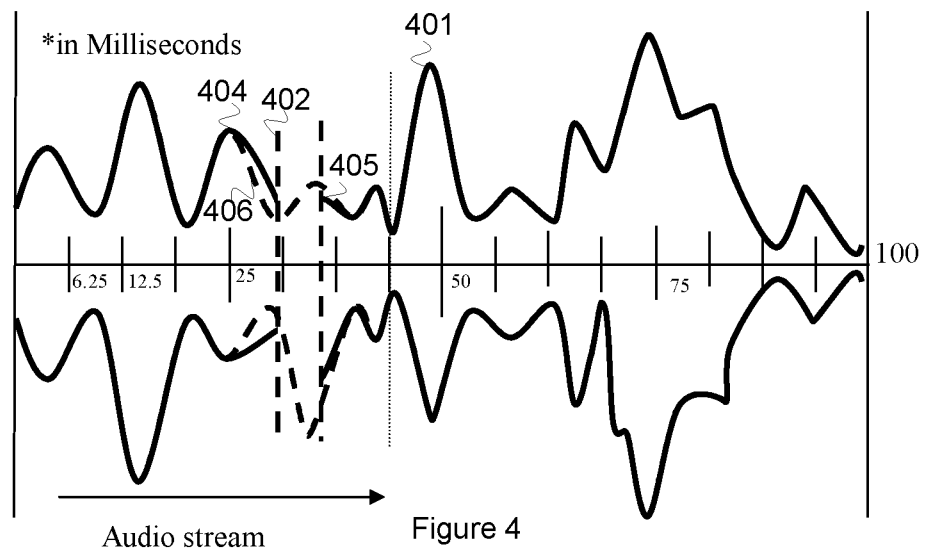


Figure 3



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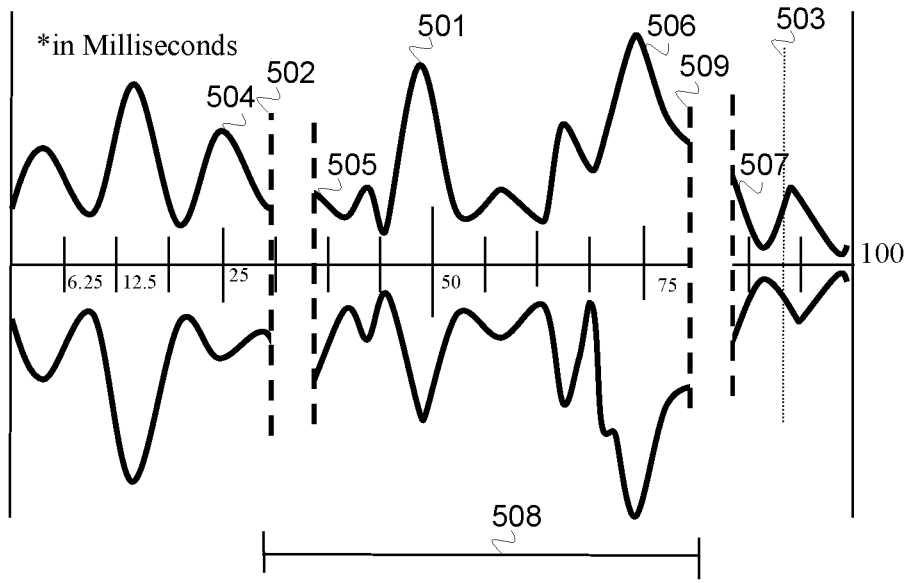


Figure 5

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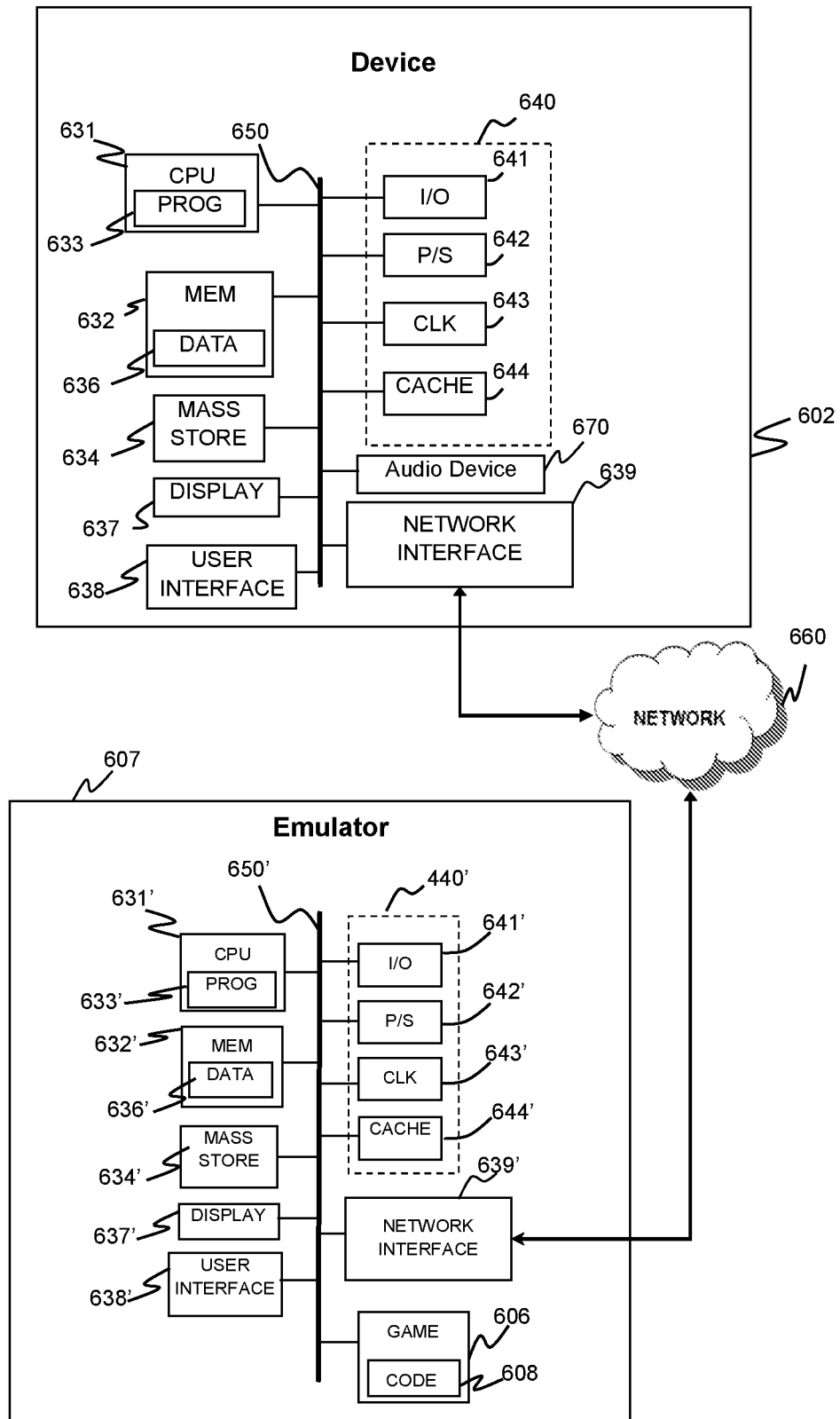


FIG. 6

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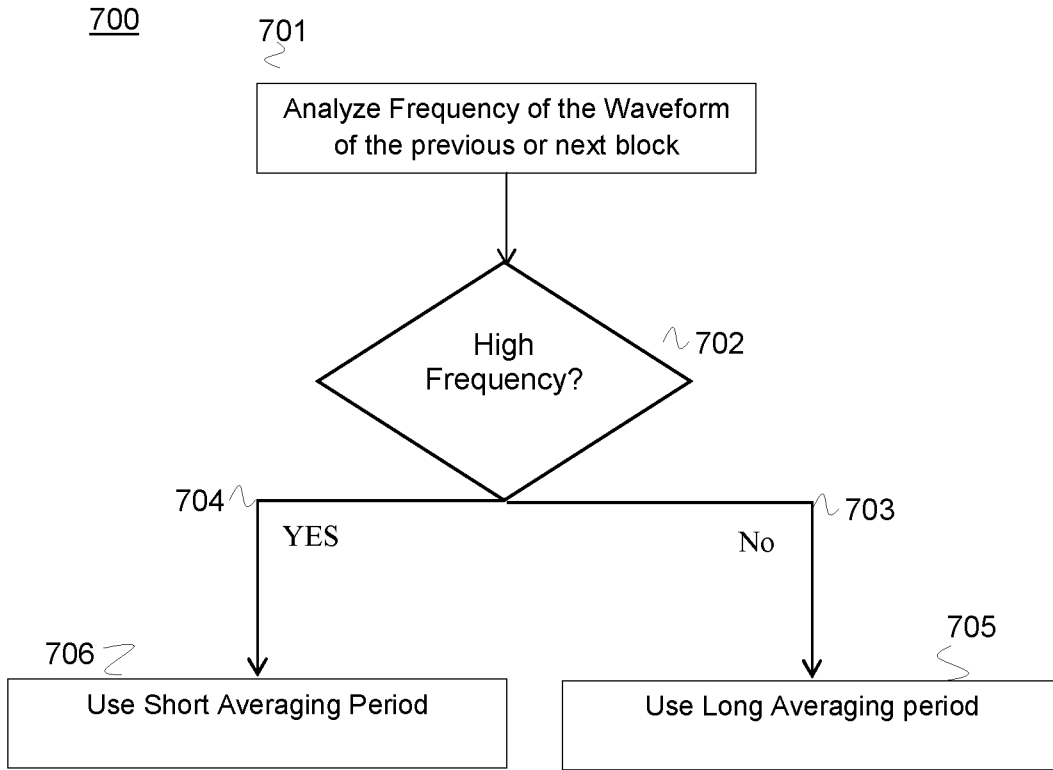
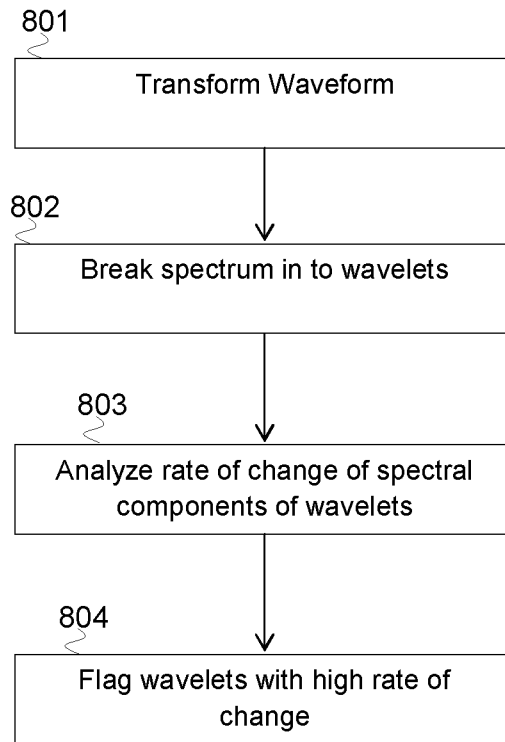


FIG. 7

800



**FIG. 8**

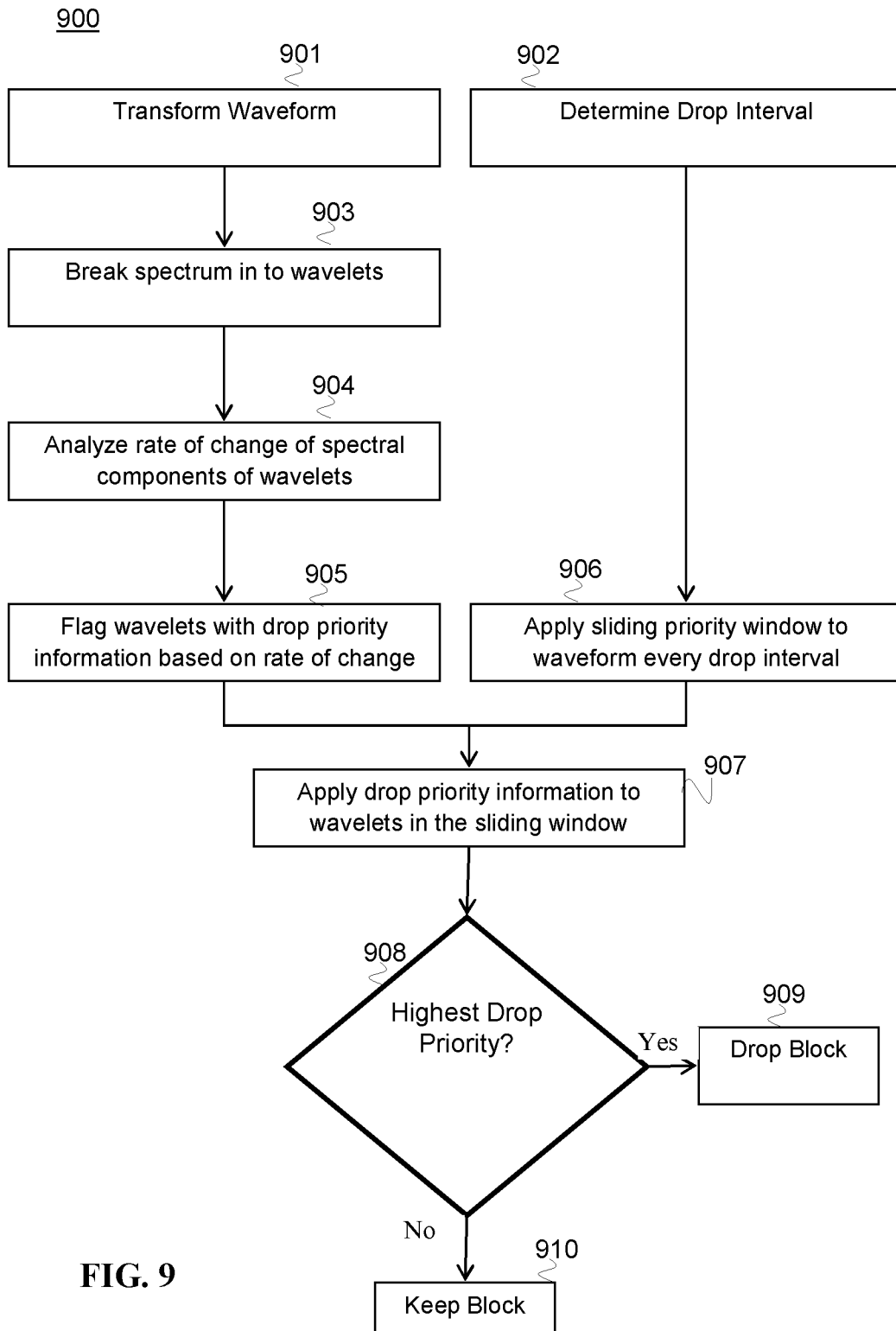


FIG. 9

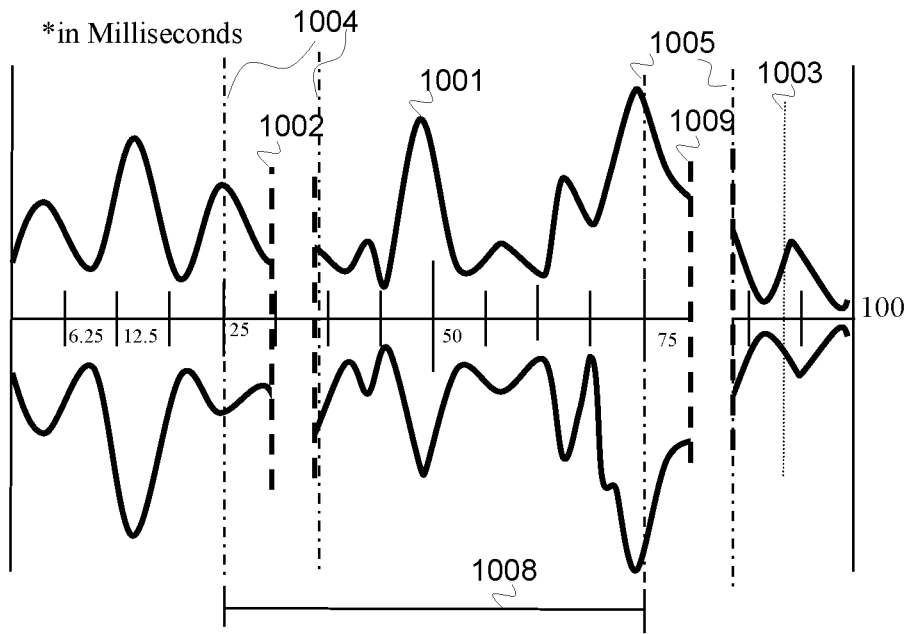


FIG. 10



## INTERNATIONAL SEARCH REPORT

International application No.

PCT/US 18/43555

## A. CLASSIFICATION OF SUBJECT MATTER

IPC(8) - H04N 21/80 (2018.01)

CPC - H04N 21/431, H04N 21/439

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

See Search History Document

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

See Search History Document

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

See Search History Document

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X --- Y --- A	US 8,560,331 B1 (PEREIRA et al.) 15 October 2013 (15.10.2013) col 5, ln 20-25, 57-59, col 7, ln 48-65, col 11, ln 26-33, col 13, ln 51-58	1, 7, 10, 19-21 ----- 2-4, 8-9, 11-14 ----- 5-6, 15-18
Y --- A	US 6,009,386 A (CRUICKSHANK et al.) 28 December 1999 (28.12.1999) col 1, ln 58-62, col 2, ln 62- col 3, ln 6, col 5, ln 27-33	2-4, 8, 11-14 ----- 5-6, 15-18
Y	US 7,130,528 B2 (BLAIR et al.) 31 October 2006 (31.10.2006) col 5, ln 11-15, 21-25	3-4, 9
Y --- A	US 5,903,872 A (FIELDER) 11 May 1999 (11.05.1999) col 11, ln 18-40, col 13, ln 2-35	14 ----- 15-18

 Further documents are listed in the continuation of Box C. See patent family annex.

\* Special categories of cited documents:

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"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

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"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&amp;" document member of the same patent family

Date of the actual completion of the international search

18 September 2018 (18.09.2018)

Date of mailing of the international search report

04 OCT 2018

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