

Information on Search Strategy - Pilot phase (see OJ 2015, A86)

The type of information contained in this sheet may change during the pilot for improving the usefulness of this new service.

Application Number

PCT/IB2018/001039

TITLE: SSD ARCHITECTURE SUPPORTING LOW LATENCY OPERATION

APPLICANT: TOSHIBA MEMORY CORPORATION

IPC CLASSIFICATION: G06F3/06

EXAMINER: Coenen, Jean Pierre

CONSULTED DATABASES: WPI, EPODOC

CLASSIFICATION SYMBOLS DEFINING EXTENT OF THE SEARCH:

IPC:

CPC: G06F3/061, G06F3/0638, G06F3/0688, G06F3/0613

FI/F-TERMS:

KEYWORDS OR OTHER ELEMENTS FEATURING THE INVENTION:

grouping memory dies/chips into sets and set an operating mode for each set, the operations on each set being performed concurrently.