

Information on Search Strategy - Pilot phase (see OJ 2015, A86)

The type of information contained in this sheet may change during the pilot for improving the usefulness of this new service.

Application Number

PCT/US2017/050861

TITLE: EQUALIZING ERASE DEPTH IN DIFFERENT BLOCKS OF MEMORY CELLS

APPLICANT: SANDISK TECHNOLOGIES LLC

IPC CLASSIFICATION: G11C16/34, G11C16/16

EXAMINER: Knack, Steffen

CONSULTED DATABASES: DOSYS, EPODOC, WPI

CLASSIFICATION SYMBOLS DEFINING EXTENT OF THE SEARCH:

IPC:

CPC: G11C16/3477, G11C16/34&153/FI, G11C16/16

FI/F-TERMS:

KEYWORDS OR OTHER ELEMENTS FEATURING THE INVENTION:

Method for erasing a NAND flash memory wherein the different distances of memory blocks to the voltage generator are compensated in order to equalise erase depth.