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(54) Title (EN): REDUCING THE IN-CTU STORAGE REQUIRED BY AFFINE INHERITANCE

(54) Title (FR): RÉDUCTION DU STOCKAGE EN UNITÉ CTU REQUISE PAR HÉRITAGE AFFINE

(57) Abstract:

(EN): Reducing the in-CTU storage required by affine inheritance is described. One example method includes associating a first group of control point motion vectors (CPMVs) for determining inherited motion information of blocks coded after a first block, with a second group of CPMVs for deriving MVs of sub-blocks of the first block or a third group of CPMVs that is signaled for the first block, wherein the first group of CPMVs is not identical with the second group of CPMVs or the third group of CPMVs; determining inherited motion information for a second block, which is coded after the first block, based on the first group of CPMVs, and performing a conversation between the second block and a bitstream representation of the second block by using the inherited motion information.

(FR): L'invention concerne la réduction du stockage en unité CTU requise par héritage affine. Un procédé représentatif comprend l'association d'un premier groupe de vecteurs de mouvement de points de commande (CPMV) pour déterminer une information de

mouvement hérité de blocs codés après un premier bloc, à un deuxième groupe de vecteurs CPMV pour dériver des vecteurs de mouvement de sous-blocs du premier bloc ou un troisième groupe de vecteurs CPMV qui est signalé pour le premier bloc, le premier groupe de vecteurs CPMV n'étant pas identique au second groupe de vecteurs CPMV ou au troisième groupe de vecteurs CPMV; la détermination d'information de mouvement hérité pour un second bloc, qui est codé après le premier bloc, sur la base du premier groupe de vecteurs CPMV, et la réalisation d'une conversation entre le second bloc et une représentation de flux binaire du second bloc au moyen de l'information de mouvement hérité.

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