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(54) Title (EN): WELLS FOR OPTIMIZED SAMPLE LOADING IN MICROFLUIDIC CHIPS

(54) Title (FR): Puits pour chargement d'échantillon optimisé dans des puces microfluidiques

(57) Abstract:

(EN): The present invention relates to a loading well (320) comprising a lateral wall part (3211) in cross-section parallel to the base plan (x/y) and/or a bottom wall part comprising at least one sloped bottom section (32121). The present invention also relates to a microfluidic chip comprising the same; systems comprising the same configured to reduce the dead volume of a drop of sample to be loaded in the microfluidic chip and/or to trap a drop in a defined location; and methods using the same.

(FR): La présente invention concerne un puits de chargement (320) comprenant une partie de paroi latérale (3211) en section transversale parallèle au plan de base (x/y) et/ou une partie de paroi inférieure comprenant au moins une section inférieure inclinée (32121). La présente invention concerne également une puce microfluidique le comprenant; des systèmes comprenant ceux-ci configurés pour réduire le volume mort d'une goutte d'échantillon à charger dans la puce microfluidique et/ou pour piéger une goutte dans un emplacement défini; et des procédés l'utilisant.

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