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(54) Title (EN): MICROFLUIDIC CHIP ARCHITECTURE WITH OPTIMIZED PHASE FLOW

(54) Title (FR): ARCHITECTURE DE PUCE MICROFLUIDIQUE AVEC ÉCOULEMENT DE PHASE OPTIMISÉ

(57) Abstract:

(EN): The present invention relates to a microfluidic chip (300) comprising an inlet channel and an output channel in close proximity; systems comprising the same configured to flow a continuous phase without disrupting the integrity of a population of dispersed phase droplets and/or to homogenize a locally static continuous phase throughout droplet loading or generation; and methods using the same.

(FR): La présente invention concerne une puce microfluidique (300) comprenant un canal d'entrée et un canal de sortie à proximité immédiate; des systèmes comprenant ceux-ci configurés pour écouler une phase continue sans perturber l'intégrité d'une population de gouttelettes en phase dispersée et/ou pour homogénéiser une phase continue localement statique tout au long du chargement ou de la génération de gouttelettes; et des procédés l'utilisant.

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