

(12) International Application Status Report

Received at International Bureau: 28 November 2019 (28.11.2019)

Information valid as of: 26 August 2020 (26.08.2020)

Report generated on: 20 September 2020 (20.09.2020)

(10) Publication number:

WO2020/109379

(43) Publication date:

04 June 2020 (04.06.2020)

(26) Publication language:

English (EN)

(21) Application Number:

PCT/EP2019/082729

(22) Filing Date:

27 November 2019 (27.11.2019)

(25) Filing language:

English (EN)

(31) Priority number(s):

62/771,854 (US)

(31) Priority date(s):

27 November 2018 (27.11.2018)

(31) Priority status:

Priority document received (in compliance with PCT Rule 17.1)

62/771,793 (US)

27 November 2018 (27.11.2018)

Priority document received (in compliance with PCT Rule 17.1)

19305419.4 (EP)

29 March 2019 (29.03.2019)

Priority document received (in compliance with PCT Rule 17.1)

19305422.8 (EP)

29 March 2019 (29.03.2019)

Priority document received (in compliance with PCT Rule 17.1)

(51) International Patent Classification:

B01L 3/00 (2006.01); **B01L 7/00** (2006.01)

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(54) Title (EN): MICROFLUIDIC CHIP ARCHITECTURE WITH OPTIMIZED PHASE FLOW

(54) Title (FR): ARCHITECTURE DE PUCE MICROFLUIDIQUE AVEC ÉCOULEMENT DE PHASE OPTIMISÉ

(57) Abstract:

(EN): The present invention relates to a microfluidic chip (300) comprising an inlet channel and an output channel in close proximity; systems comprising the same configured to flow a continuous phase without disrupting the integrity of a population of dispersed phase droplets and/or to homogenize a locally static continuous phase throughout droplet loading or generation; and methods using the same.

(FR): La présente invention concerne une puce microfluidique (300) comprenant un canal d'entrée et un canal de sortie à proximité immédiate; des systèmes comprenant ceux-ci configurés pour écouler une phase continue sans perturber l'intégrité d'une population de gouttelettes en phase dispersée et/ou pour homogénéiser une phase continue localement statique tout au long du chargement ou de la génération de gouttelettes; et des procédés l'utilisant.

International search report:

Received at International Bureau: 03 February 2020 (03.02.2020) [EP]

International Report on Patentability (IPRP) Chapter II of the PCT:

Chapter II demand received: 09 July 2020 (09.07.2020)

(81) Designated States:

AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JO, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW

European Patent Office (EPO) : AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR

African Intellectual Property Organization (OAPI) : BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG

African Regional Intellectual Property Organization (ARIPO) : BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW

Eurasian Patent Organization (EAPO) : AM, AZ, BY, KG, KZ, RU, TJ, TM

Declarations:

Declaration of inventorship (Rules 4.17(iv) and 51bis.1(a)(iv)) for the purposes of the designation of the United States of America